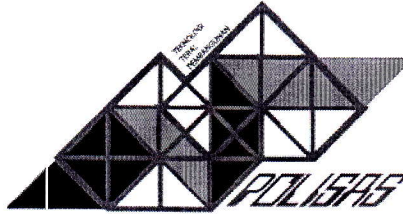


Approved by:

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**POLITEKNIK SULTAN HAJI AHMAD SHAH**  
**ELECTRICAL ENGINEERING DEPARTMENT**

**E4160**

**Microprocessor & Microcontroller System**

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**Assignment 2**

1. Show how the flag register is affected by the following instructions:

- i.     MOVLW     B'00001111'  
       ANDLW     B'10110000'
  
- ii.    MOVLW     B'00110001'  
       IORLW     B'11001110'
  
- iii.   MOVLW     B'11111111'  
       ADDLW     B'00000001'
  
- iv.    MOVLW     B'11111000'  
       MOVWF     Mybyte             ;Mybyte is a variable declared in GPR  
       MOVLW     B'10001001'  
       ADDWF     Mybyte,0
  
- v.     MOVLW     B'00001111'  
       MOVWF     MyReg             ;MyReg is a variable declared in GRP  
       MOVLW     B'00001111'  
       SUBWF     MyReg, 0

2. State the contents of the file register RAM locations after the following program:

```
MOVLW     H'99'             ;load WREG with value H'99'  
MOVWF     H'12'  
MOVLW     H'85'             ;load WREG with value H'85'
```